

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	94	(level) with cache with instruction with (predict or prediction or predicted or history or histories or past)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/3 0 09:45
2	BRS	L2	46	(level) adj5 cache with instruction with (predict or prediction or predicted or history or histories or past)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/3 0 09:52
3	BRS	L3	1	((level or hierarchy or hierarchical) adj5 cache) with instruction with (predict or prediction or predicted or history or histories or past) with (parallel or simultaneous or simultaneously or "same" adj3 time)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/3 0 09:55
4	BRS	L4	9	((level or hierarchy or hierarchical) adj5 cache) with instruction same (predict or prediction or predicted or history or histories or past) same (parallel or simultaneous or simultaneously or "same" adj3 time)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/3 0 10:14

	Type	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L5	9	((level or hierarchy or hierarchical) adj5 cache) with instruction same (predict or prediction or predicted or predicting or history or histories or past) same (parallel or simultaneous or simultaneously or "same" adj3 time)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/30 10:15
6	BRS	L6	43	((level or hierarchy or hierarchical) adj5 cache) same instruction same (predict or prediction or predicted or predicting or history or histories or past) same (parallel or simultaneous or simultaneously or "same" adj3 time)	USP AT; US-P GPU B; EPO; JPO; IBM_ TDB	2004/01/30 10:15